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FORM PTO-1449 (Modified) LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)			Attorney Docket No.: 016301-035000US		Application No.: 09/759,854	
			Applicant: Christopher Ngai, et 1.			
			Filing Date: January 12, 2001		Group: 2812 2 8 2 3	
R ference Desig	mation	1	U.S. PATENT DOCUMEN	TS		Page 1
Examiner Initial	Document No.	Date	Name	Class	Sub-class	Filing Date (If Appropriate)
		FOI	 EEIGN PATENT DOCUM	ENTS		
	Document No.	Date	Country	Class	Sub-class	Translation (Yes/No)
	OTE	ER ART (Incl	ding Author, Title, Date,	Pertinent Pages,	Etc.)	
WB A	Baliga, John, "New Materials Enhance Memory Performance," Semiconductor International (November 1999) Pages: 79-90.					
<i>%</i> ₿_ в	DeJule, Ruth, "SOI Comes of Age," Semiconductor International (November 1999) Pages: 67-74.					
₩Ł C	Peters, Laura, "Solving the Integration Challenges of Low-k Dielectrics," Semiconductor International (November 1999) Pages: 56-64.					
<u>9vb</u> D	Peters, Laura, "Pursuing the Perfect Low-k Dielectric," Semiconductor International (September 1998) Pages: 64-74.					
<u>9v/8</u> E	Singer, Peter, "Dual-Damascene Challenges Dielectric Etch," Semiconductor International (August 1999) Pages: 68-72.					
EXAMINER	Walliam 93	1. Brunds	DATE CONSIDERED	20(183		

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.